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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **Hiroshi HASHIMOTO et al.**

Group Art Unit: **2814**

Application Number: **10/083,533**

Examiner: **Thao X. LE**

Filed: **February 27, 2002**

Confirmation Number: **6400**

For: **SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE INCLUDING A
SEMICONDUCTOR DEVICE HAVING A STABLE THRESHOLD
CHARACTERISTIC**

Attorney Docket Number: **020244**

Customer Number: **38834**

RESPONSE UNDER 37 C.F.R. §1.116

Mail Stop AF
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

June 8, 2007

Sir:

This paper is filed in response to the Office Action dated March 9, 2007.

Remarks/Arguments begin on page 2 of this paper.